

NAVOLCHI

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TU / **e** Technische Universiteit
Eindhoven
University of Technology

Where innovation starts

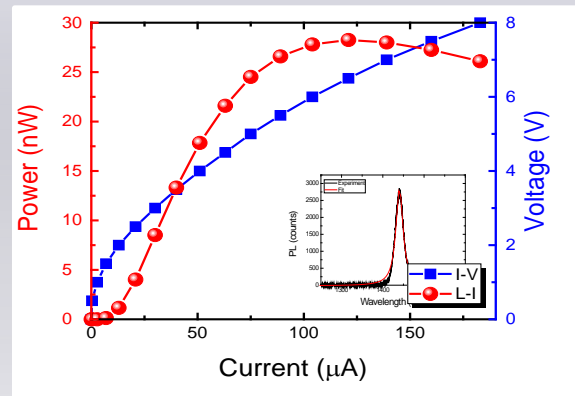
Milestones and Deliverables

MS40	Individual plasmonic devices characterization, testing and evaluation	WP6
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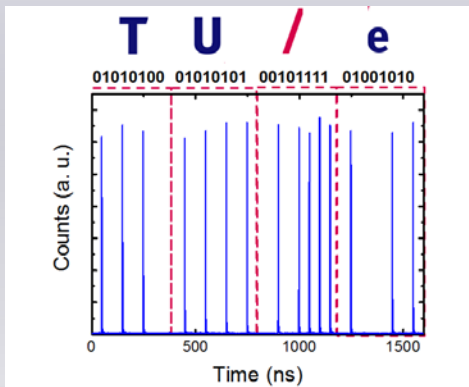
- Contents of D6.1 will be used (Report on characterization results of all plasmonic devices).
- An e-mail will follow requesting additional input (if any)

Outline

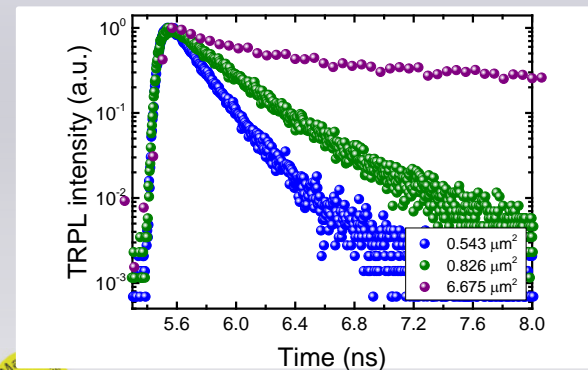
1. Electro-optical characterization



2. Direct electrical modulation of nanoLEDs

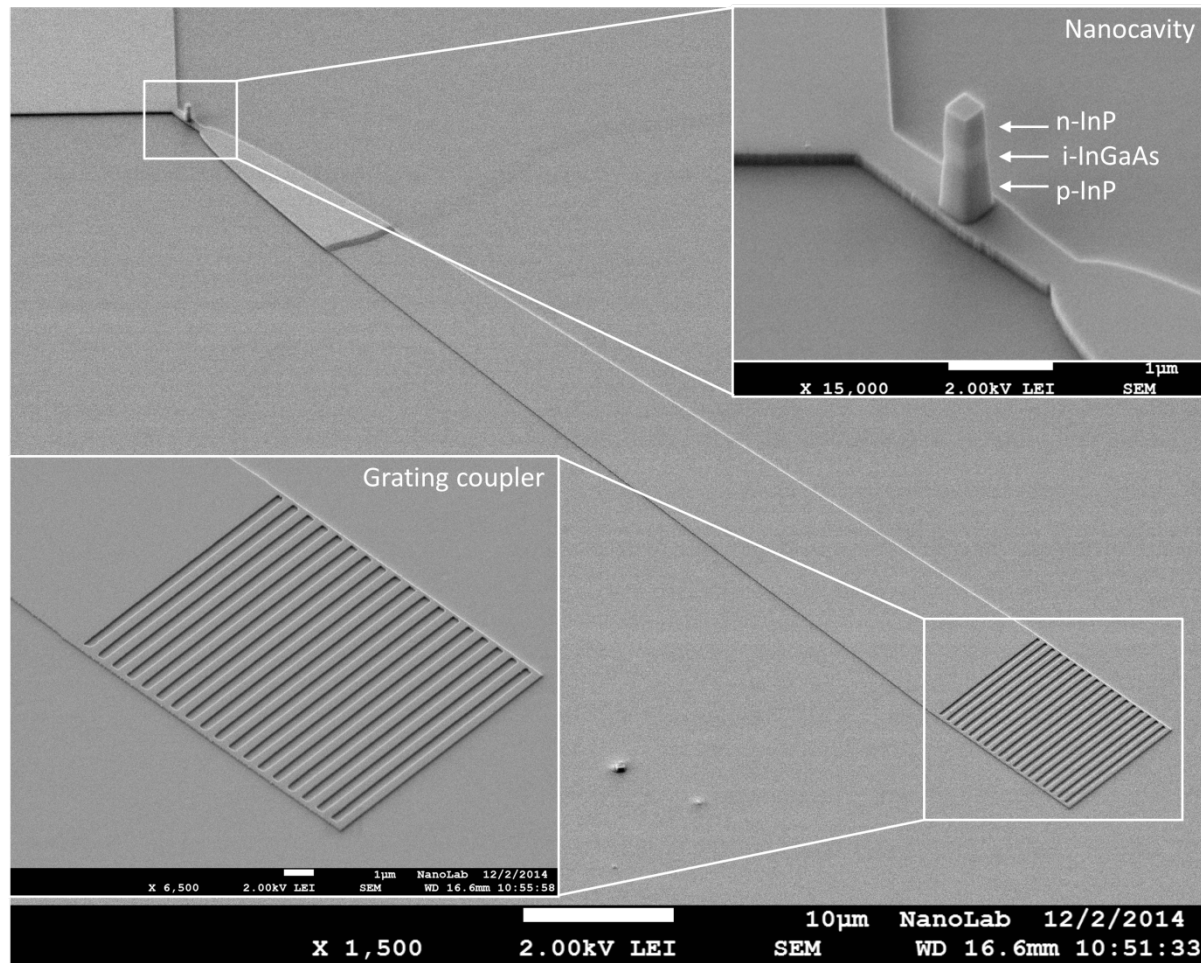


3. Optically pumped time-resolved measurements of InGaAs nanopillars



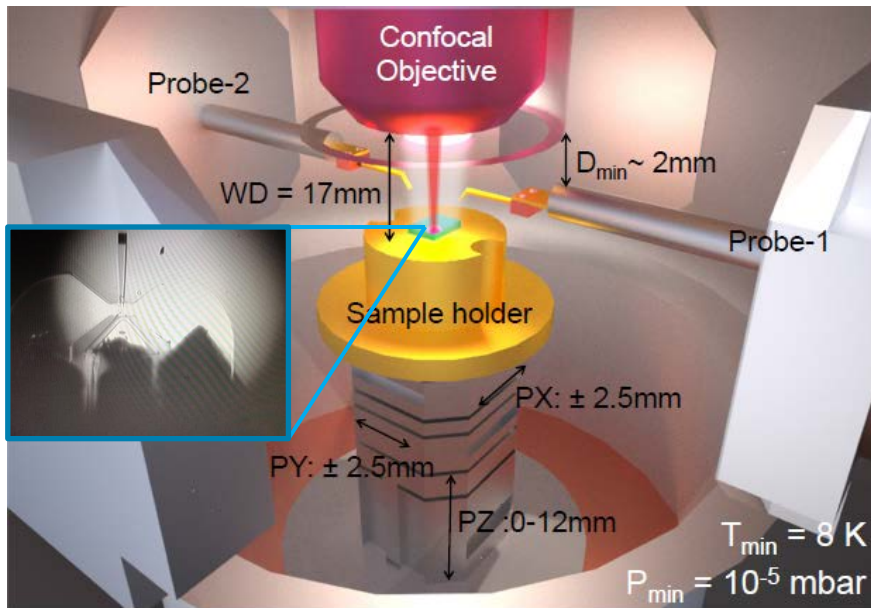
Integrated nanoLED for photonic circuits

- The metal-cavity nanoLED was fabricated in a III-V layer stack bonded to a silicon wafer

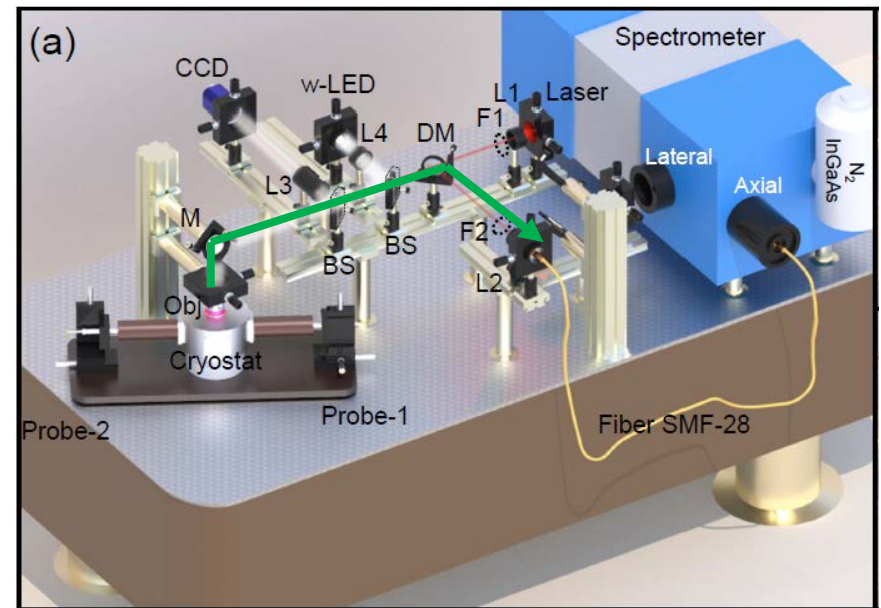


Experimental characterization setup

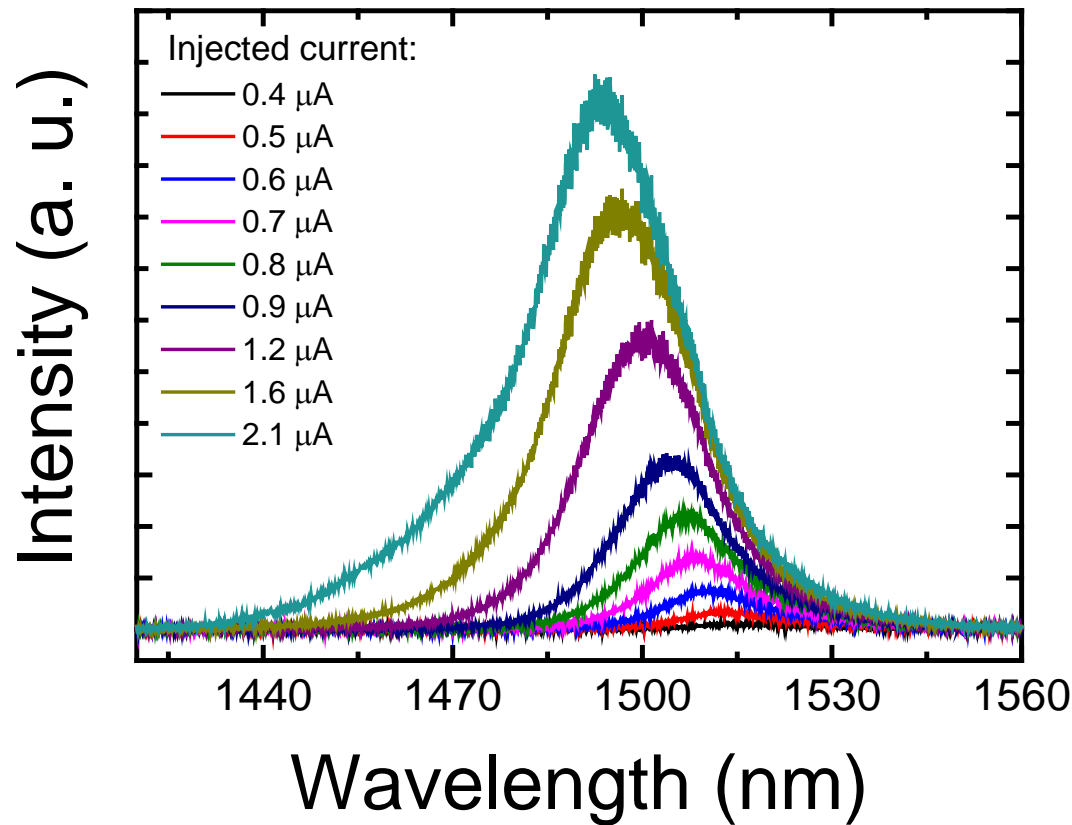
- Electro-luminescence setup with a cryogenic chamber
- Temperatures: RT, 150K, 75K, 9K
- Collection spot $\sim 2 \mu\text{m}$ (grating coupler $\sim 10 \times 12 \mu\text{m}^2$)
- Maximum collection angle: 23°



F. Pagliano, PhD thesis, 2014.

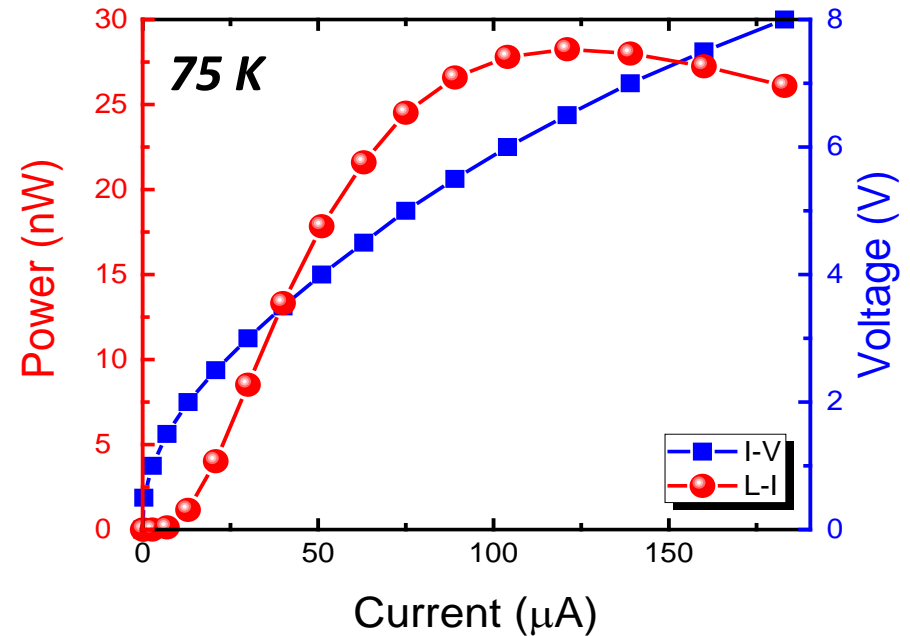
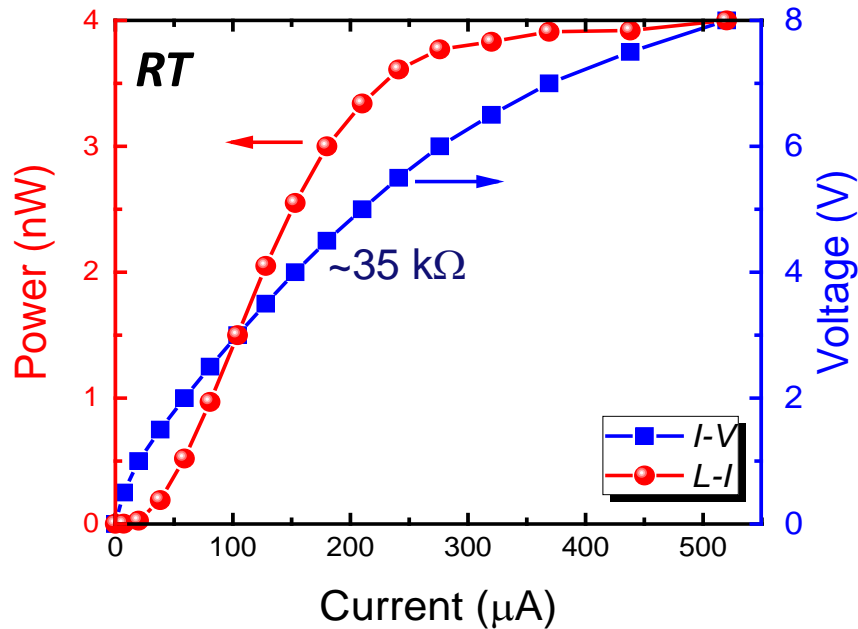


Emission spectra



- Operation at telecommunications wavelengths (~ 1.5 μm);
- FWHM ranging from 20 nm to 50 nm at low injection currents;
- Wavelength blueshift (carrier induced change in the refractive index)

L-I-V characteristics

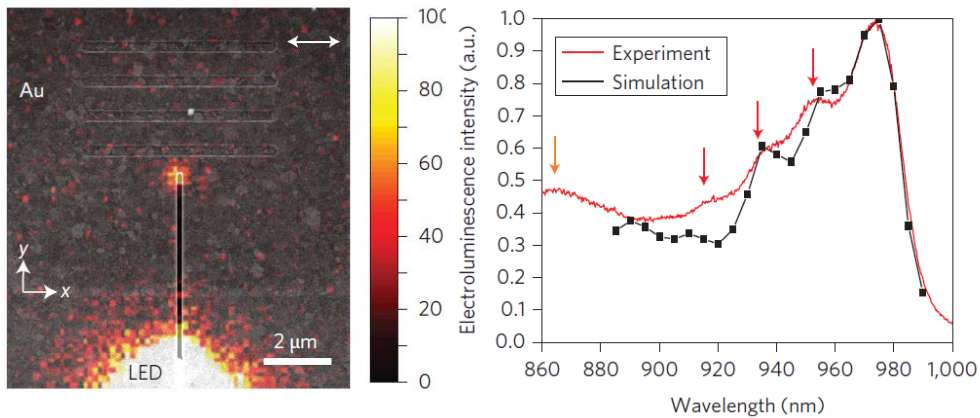


- Smooth turn-on voltage:
→ Leakage current?
- Max. power: **4 nW**

- Shifted turn-on voltage due to low temperature
- Max. power: **28 nW**
- Efficiencies $\sim 10^{-3} \text{ W/A}$

Nano-sources in the literature

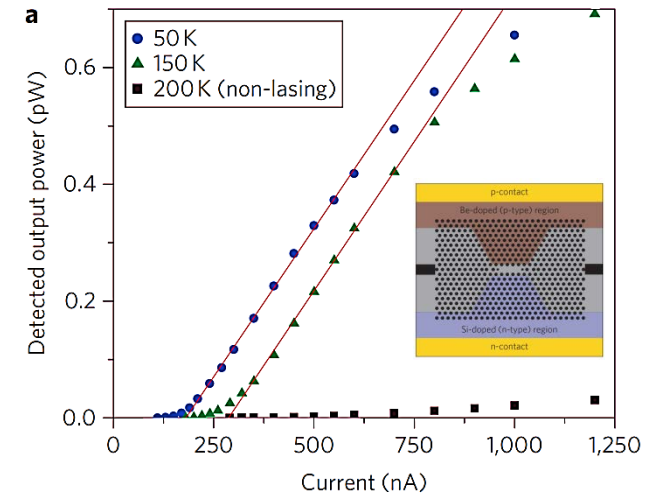
Powers in arbitrary units



Plasmonic light-emitting diode

(Kevin C. Y. Huang et al., *Nature Photonics* 8, 2014)

Powers below 1 pW!



Electrically pumped photonic crystals

(Bryan Ellis et al., *Nature Photonics* 5, 2011)

Reported efficiencies in the literature spanning from 10^{-5} to 10^{-7}

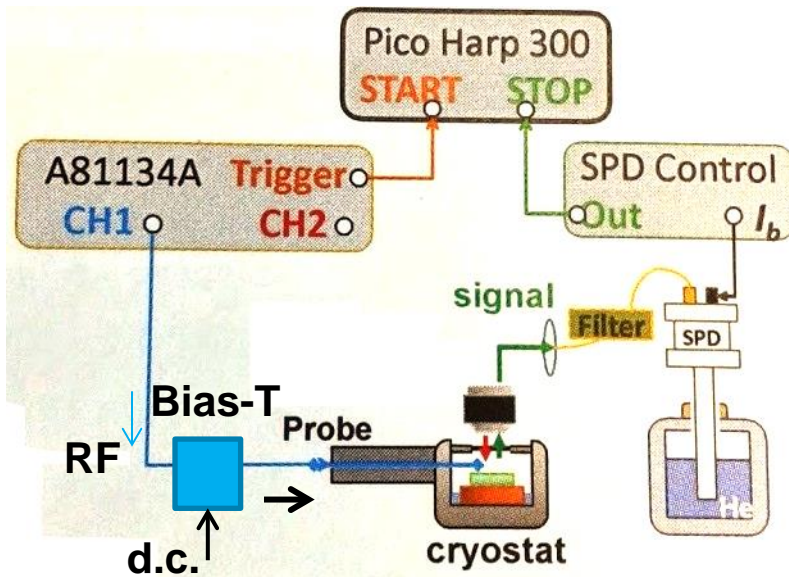
We achieved 10s of nW at 10s uW bias levels

⇒ 10^{-3}

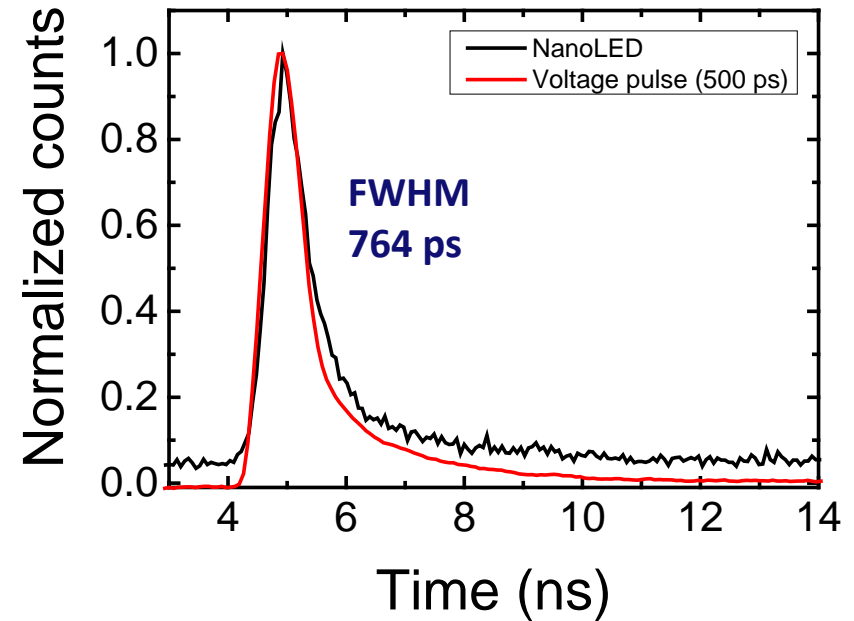


Direct electrical modulation of nanoLED

Time resolved experimental setup



Room-temperature (bias current of 28 μA)



- Sub-nanosecond FWHM (~ 0.5 ns minimum)
- Potential to operate at Gb/s speeds.

''Encoding'' a message in a nanoLED

TU/e

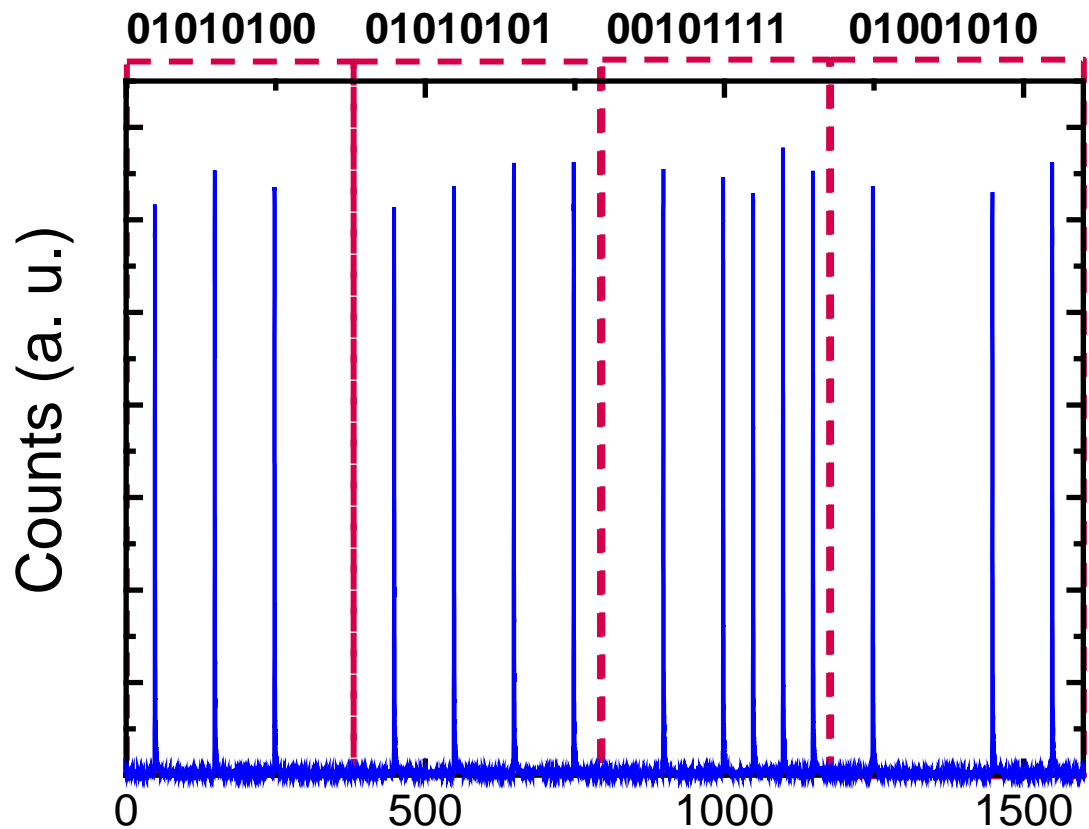
T U / e

Message sent
in a nanoLED:

Hundreds of MB/s,
potentially Gb's

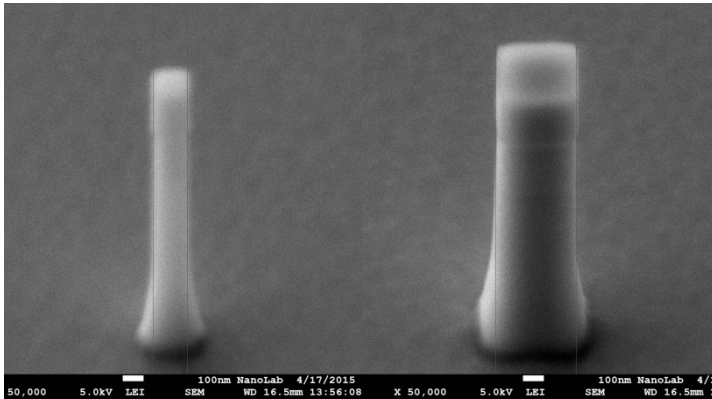
→ Ongoing

This is a statistical measurement!



Photoluminescence in InGaAs nanopillars

- SEM pictures of the smallest pillars



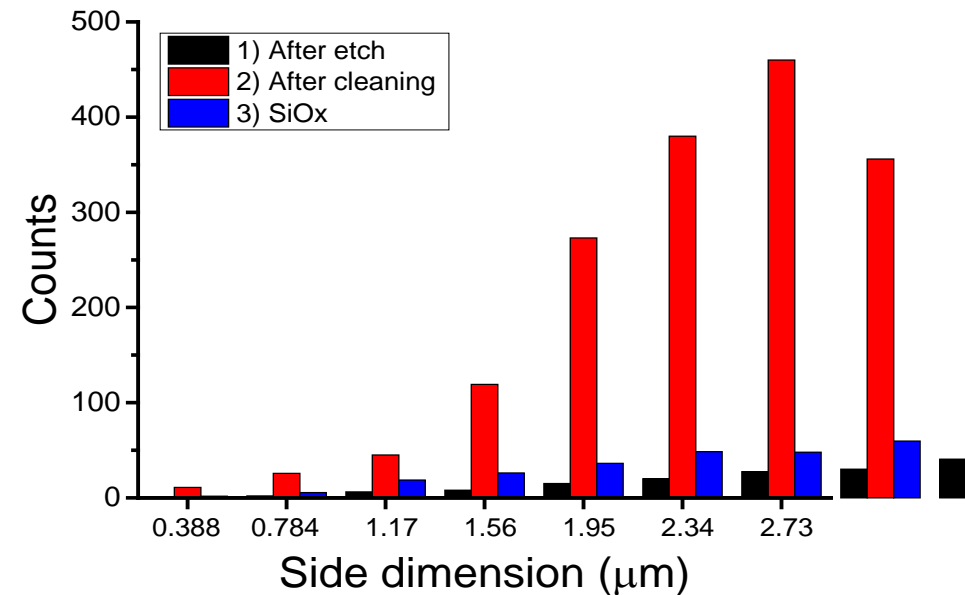
- Layer stack

	InP	350
	InGaAs	350
	InP	1000
	InP	Substrate

- Steps

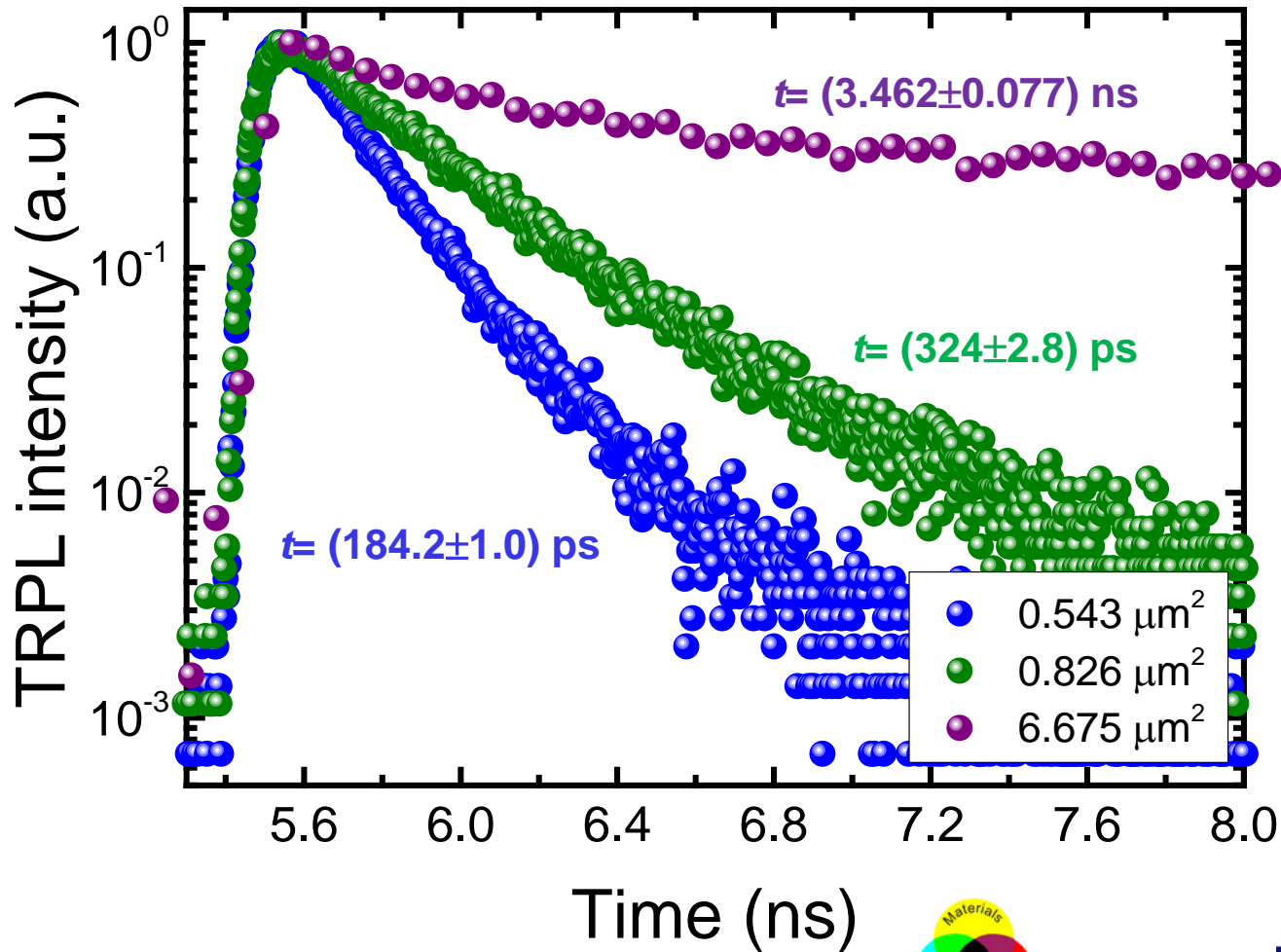
- 1) Etching+O₂
- 2) Cleaning
- 3) SiO_x deposition @ 300°C + annealing

- PL summary results

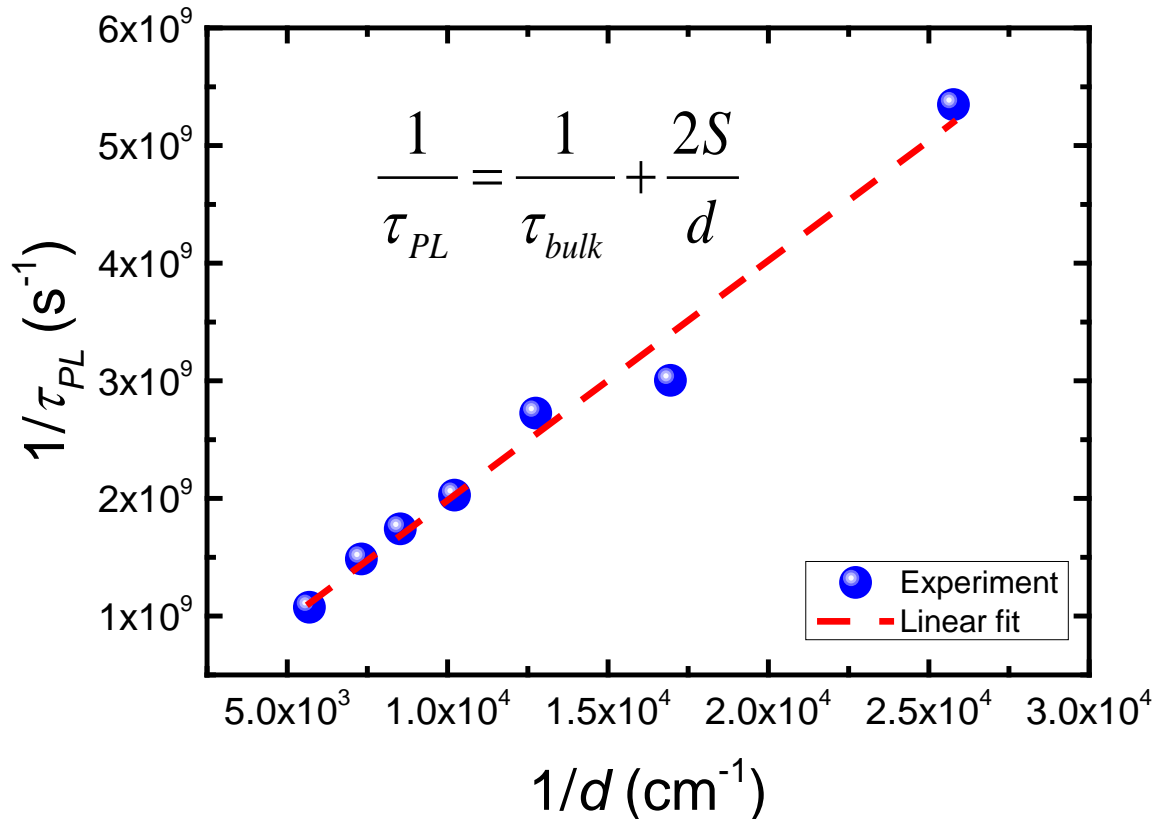


- The deposition of SiO_x at high temperature is strongly affecting the optical properties of the pillars

Decay time vs. pillar size



Surface recombination and bulk lifetime



Extracted parameters:

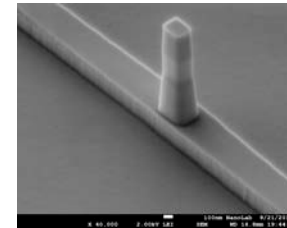
$$S \sim 1 \times 10^5 \text{ cm s}^{-1}$$

$$\tau_{bulk} \sim 4.8 \text{ ns}$$

- Large surface recombination indicating strong non-radiative losses;
- The presence of a fast non-radiative recombination gives rise to the possibility of a high-speed operation at room temperature.

Conclusion

- Demonstration of an electrically pumped metal cavity nanoLED coupled to a InP-waveguide on silicon.



- Possibility of a fast, efficient, nanoscale source for on-chip communication:

- ✓ Operation at telecommunication wavelengths ($\sim 1.5 \mu\text{m}$);
- ✓ Measured output powers of tens of nW and efficiencies up to $\sim 10^{-3} \text{ W/A}$;
- ✓ Sub-nanosecond direct electrical modulation.

